

09636209\_CLS

Most Frequently Occurring Classifications of Patents Returned  
From A Search of 09636209 on October 15, 2002

Original Classifications

2 710/23  
2 711/141  
2 711/145  
2 712/244

Cross-Reference Classifications

3 711/144  
3 711/146  
2 345/531  
2 369/53.24  
2 370/412  
2 710/26  
2 711/141  
2 711/143  
2 711/158  
2 712/222

Combined Classifications

4 711/141  
3 370/412  
3 711/143  
3 711/144  
3 711/146  
2 345/422  
2 345/531  
2 345/545  
2 345/570  
2 358/1.16  
2 358/1.17  
2 369/53.24  
2 370/395.7  
2 710/22  
2 710/23  
2 710/26  
2 711/113  
2 711/145  
2 711/158  
2 712/222  
2 712/244

09636209\_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned

From A Search of 09636209 on October 15, 2002

- 4 711/141 (2 OR, 2 XR)
  - Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
  - 711/100 STORAGE ACCESSING AND CONTROL
  - 711/117 .Hierarchical memories
  - 711/118 ..Caching
  - 711/141 ...Coherency
- 3 370/412 (1 OR, 2 XR)
  - Class 370 : MULTIPLEX COMMUNICATIONS
  - 370/351 PATHFINDING OR ROUTING
  - 370/389 .Switching a message which includes an address header
  - 370/412 ..Queuing arrangement
- 3 711/143 (1 OR, 2 XR)
  - Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
  - 711/100 STORAGE ACCESSING AND CONTROL
  - 711/117 .Hierarchical memories
  - 711/118 ..Caching
  - 711/141 ...Coherency
  - 711/143 ....Write-back
- 3 711/144 (0 OR, 3 XR)
  - Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
  - 711/100 STORAGE ACCESSING AND CONTROL
  - 711/117 .Hierarchical memories
  - 711/118 ..Caching
  - 711/141 ...Coherency
  - 711/144 ....Cache status data bit
- 3 711/146 (0 OR, 3 XR)
  - Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING SYSTEMS: MEMORY
  - 711/100 STORAGE ACCESSING AND CONTROL
  - 711/117 .Hierarchical memories
  - 711/118 ..Caching
  - 711/141 ...Coherency
  - 711/146 ....Snooping
- 2 345/422 (1 OR, 1 XR)

09636209\_CLSTITLES

Class 345 : COMPUTER GRAPHICS PROCESSING, OPERATOR  
INTERFACE PROCESSING, AND SELECTIVE VISUAL  
DISPLAY

SYSTEMS

345/418 COMPUTER GRAPHICS PROCESSING

345/419 .Three-dimension

345/421 ..Hidden line/surface determining

345/422 ...Z buffer (depth buffer)

2 345/531 (0 OR, 2 XR)

Class 345 : COMPUTER GRAPHICS PROCESSING, OPERATOR  
INTERFACE PROCESSING, AND SELECTIVE VISUAL  
DISPLAY

SYSTEMS

345/530 COMPUTER GRAPHICS DISPLAY MEMORY SYSTEM

345/531 .Graphic display memory controller

2 345/545 (1 OR, 1 XR)

Class 345 : COMPUTER GRAPHICS PROCESSING, OPERATOR  
INTERFACE PROCESSING, AND SELECTIVE VISUAL  
DISPLAY

SYSTEMS

345/538 ...Data transfer between system memory display  
memory

345/545 .Frame buffer

2 345/570 (1 OR, 1 XR)

Class 345 : COMPUTER GRAPHICS PROCESSING, OPERATOR  
INTERFACE PROCESSING, AND SELECTIVE VISUAL  
DISPLAY

SYSTEMS

345/538 ...Data transfer between system memory display  
memory

345/564 .Addressing

345/570 ..Page mode

2 358/1.16 (1 OR, 1 XR)

Class 358 : FACSIMILE AND STATIC PRESENTATION PROCESSING

358/1.1 STATIC PRESENTATION PROCESSING (E.G.,  
PROCESSING DATA FOR PRINTER, ETC.)

358/1.16 .Memory

2 358/1.17 (1 OR, 1 XR)

Class 358 : FACSIMILE AND STATIC PRESENTATION PROCESSING

09636209\_CLSTITLES

358/1.1           STATIC PRESENTATION PROCESSING (E.G.,  
                                PROCESSING DATA FOR PRINTER, ETC.)

358/1.16           .Memory

358/1.17           ..Page or frame memory

2   369/53.24       (0 OR, 2 XR)

      Class    369 :   DYNAMIC INFORMATION STORAGE OR RETRIEVAL

      369/53.1       CONDITION INDICATING, MONITORING, OR TESTING

      369/53.11      .Including radiation storage or retrieval

      369/53.2       ..Of record carrier

      369/53.24      ...Having unrecorded location indicating

2   370/395.7       (1 OR, 1 XR)

      Class    370 :   MULTIPLEX COMMUNICATIONS

      370/351        PATHFINDING OR ROUTING

      370/389        .Switching a message which includes an address  
                                header

      370/395.1      ..Message transmitted using fixed length  
                                packets (e.g., ATM cells)

      370/395.7      ...Having detail of switch memory  
                                reading/writing

2   710/22           (1 OR, 1 XR)

      Class    710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA  
                                PROCESSING SYSTEMS: INPUT/OUTPUT

      710/1           INPUT/OUTPUT DATA PROCESSING

      710/22          .Direct Memory Accessing (DMA)

2   710/23           (2 OR, 0 XR)

      Class    710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA  
                                PROCESSING SYSTEMS: INPUT/OUTPUT

      710/1           INPUT/OUTPUT DATA PROCESSING

      710/22          .Direct Memory Accessing (DMA)

      710/23          ..Programmed control memory accessing

2   710/26           (0 OR, 2 XR)

      Class    710 :   ELECTRICAL COMPUTERS AND DIGITAL DATA  
                                PROCESSING SYSTEMS: INPUT/OUTPUT

      710/1           INPUT/OUTPUT DATA PROCESSING

      710/22          .Direct Memory Accessing (DMA)

      710/26          ..Using addressing

2   711/113          (1 OR, 1 XR)

      Class    711 :   ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
                                SYSTEMS: MEMORY

      711/100         STORAGE ACCESSING AND CONTROL

      711/101         .Specific memory composition

      711/111         ..Accessing dynamic storage device

09636209\_CLSTITLES

711/112 ...Direct access storage device (DASD)  
711/113 ....Caching

2 711/145 (2 OR, 0 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: MEMORY

711/100 STORAGE ACCESSING AND CONTROL  
711/117 .Hierarchical memories  
711/118 ..Caching  
711/141 ...Coherency  
711/145 ....Access control bit

2 711/158 (0 OR, 2 XR)

Class 711 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: MEMORY

711/100 STORAGE ACCESSING AND CONTROL  
711/154 .Control technique  
711/158 ..Prioritizing

2 712/222 (0 OR, 2 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

PROCESSING  
712/220 PROCESSING CONTROL  
712/221 .Arithmetic operation instruction processing  
712/222 ..Floating point or vector

2 712/244 (2 OR, 0 XR)

Class 712 : ELECTRICAL COMPUTERS AND DIGITAL PROCESSING  
SYSTEMS: PROCESSING ARCHITECTURES AND INS

TRUCTION

PROCESSING  
712/220 PROCESSING CONTROL  
712/233 .Branching (e.g., delayed branch, loop control  
branch predict, interrupt)  
712/244 ..Exeception processing (e.g., interrupts and  
traps)

09636209\_LIST

PLUS Search Results for S/N 09636209, Searched October 15, 2002

4910731  
RE34305  
5483526  
5602853  
5633867  
5978810  
6021139  
5627988  
6216222  
6216222  
5909440  
6286074  
6317800  
6324669  
5341476  
5860112  
6091428  
6202136  
4912670  
5317553  
5371838  
5537128  
5577193  
5590087  
5717952  
5774434  
5908392  
5917947  
5949973  
6052346  
6061765  
6061765  
6101581  
6145043  
6154793  
4482951  
4603380  
4623997  
4625307  
4866717  
4951143  
4961153  
4970501  
5003471  
5193153  
5197032

09636209\_LIST

5285527  
5325469  
5369651  
5392241

09636209\_QUAL

4910731 86  
RE34305 86  
5483526 86  
5602853 86  
5633867 86  
5978810 86  
6021139 86  
5627988 85  
6216222 85  
6216222 85  
5909440 80  
6286074 80  
6317800 80  
6324669 80  
5341476 72  
5860112 72  
6091428 72  
6202136 72  
4912670 72  
5317553 72  
5371838 72  
5537128 72  
5577193 72  
5590087 72  
5717952 72  
5774434 72  
5908392 72  
5917947 72  
5949973 72  
6052346 72  
6061765 72  
6061765 72  
6101581 72  
6145043 72  
6154793 72  
4482951 72  
4603380 72  
4623997 72  
4625307 72  
4866717 72  
4951143 72  
4961153 72  
4970501 72  
5003471 72  
5193153 72  
5197032 72  
5285527 72  
5325469 72



09636209\_QUAL

5369651 72  
5392241 72